Computer Architecture Midterm Exam Solution

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Computer Architecture Midterm Exam Solution

EXAM 1 SOLUTIONS Problem 2 (20 points) You designed a microprocessor. It came back from the fab with an error: one of the bits is stuck. We call the bit a stuck-at-0 fault if the bit is always 0 (i.e., you cannot store a 1 in it).

EXAM 1 SOLUTIONS

CSE 490/590 Computer Architecture Midterm Solution. DIRECTIONS Time limit: 45 minutes (12pm - 12:45pm) There are 40 points plus 5 bonus points. This is a closed-book, no calculator, closed-notes exam. Each problem starts on a new page. Please use a pen, not a pencil.

CSE 490/590 Computer Architecture Midterm Solution

CS252 Graduate Computer Architecture Midterm 1 Solutions. Part A: Branch Prediction (22 Points) Consider a fetch pipeline based on the UltraSparc-III processor (as seen in Lecture 5). In this part, we evaluate the impact of branch prediction on the processor's performance. Assume there are no branch delay slots.

CS252 Graduate Computer Architecture Midterm 1 Solutions

COMP 212 Computer Architecture Mid-term Exam Fall 2008 To

be fair, please do NOT open the exam book, until told so. Notice: Mid-term is close book, close notes, NO calculator and NO discussions. Please write down the details of your solutions, partial results will be given partial credits. Don't rush, you should have plenty of time, do a careful

COMP 212 Computer Architecture Mid-term Exam Fall 2008

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exams [Computer Architecture - Fall 2019]

• You have about 90 minutes for the exam (avg. 15 minutes per problem). • There are 9 pages in the exam (including this one), plus a 1-page answer sheet for problem number 6. Please ensure

you have all pages. • Be sure to show work and explain what you've done when asked to do so.

EECS 470 Midterm Exam - Solutions - WordPress.com

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exams [Computer Architecture - Fall 2017]

ECM534 Advanced Computer Architecture. Instructor: Prof. Taeweon Suh. Fall 2019. Time: ... Digital Design and Computer Architecture by David M. Harris and Sarah L. Harris, Morgan Kaufmann, 2012. Reference: Computer Organization and Design by David Patterson and John Hennessy, 5th edition, ... Quiz#2 solution. Midterm Exam, ...

ECM534 Advanced Computer Architecture - Korea University

Solution: Given that the physical address is 20 bits long, and the tag is 11 bits, there are 9 bits left over for the index and offset We can determine the number of bits of offset as the problem states that: - Data is word addressable and words are 8 bits long - Each block holds 16 bytes

CSE 30321 – Computer Architecture I – Fall 2010 Final Exam ...

CSE 30321 – Computer Architecture I – Fall 2010 Midterm Exam October 14, 2010 Test Guidelines: 1. Place your name – or at least your initials! – on ***EACH*** page of the test in the space provided. Be sure to do this on p. 1 and 2! 2. Answer every question in the space provided. If separate sheets are needed, make sure to

2010 Midterm Key - University of Notre Dame

Computer Architecture 1 Fall 2011 Final Exam Solutions, Uppsala University Page 2 of 8 False/True&[6points]& Circle either false or true or neither. 0 points for no answer, -1 point for an incorrect answer, +1 point for a correct answer. 4a. For forwarding you need only look at the data available in the WB stage. False True False.

Dark 1 HT2011 Exam Solutions - Uppsala University

CSE 410 sp06 Midterm Exam 10. [3] Imagine that you have an electronic sensor that reports its measurements in an 8-bit byte, formatted as shown here. The low order bit (bit 0) is the status bit. Bits 1, 2, and 3 are not used. Bits 4 through 7 contain the measurement that the device is reporting. measurement value (4 bits) unused (3 bits) (1 bit)

courses.cs.washington.edu

The solution appears on the previous page. Looking at the code execution example, notice that in the original pipeline (see the previous page) at cycle 3 the load instruction must be placing its weand fdvalues in the A2stage, but that would replace (clobber)

ComputerArchitecture EE4720 MidtermExamination

1.This is a closed book exam. You are allowed to have one letter-sized cheat sheet. 2.No electronic devices may be used. 3.This exam lasts 1 hour and 50 minutes. 4.Clearly indicate your nal answer for each problem. 5.Please show your work when needed. 6.Please write your initials at the top of every page.

SOLUTIONS - Electrical and Computer Engineering at ...

Access study documents, get answers to your study questions, and connect with real tutors for CSC 4210 : Computer Architecture at Georgia State University.

CSC 4210 : Computer Architecture - GSU

1 Prof. Martin Thursday, March 15th, 2012 CIS371 – Computer Organization and Design Midterm Exam Solutions 1. [11 Points] Short Answer. (a) Give two different reasons why increasing the die (chip) size of a microprocessor increases its

CIS371 – Computer Organization and Design Midterm Exam ...

Computer Engineering Department COE 403 – Fall 2016 Computer Architecture Midterm Exam Saturday, November 26, 2016 10 am – 12 noon Prepared by: Dr. Muhamed Mudawar Student Name: SOLUTION Student ID: Q1 / 15 Q2 / 30 Q3 / 20 Q4 / 15 Q5 / 10 Q6 / 15 Total / 105 . Page 2 of 10 Q1. (15 pts) Consider the following loop.

COE 403 – Fall 2016 Computer Architecture

The exam section contains 5 quiz files for the course. Subscribe

to the OCW Newsletter: Help ... Engineering and Computer Science » Computer System Architecture » Exams ... Instructors may request the solutions for these assignments by using the MIT OpenCourseWare Feedback form. Quiz 1 (PDF - 1.5 MB)

Exams | Computer System Architecture | Electrical ...

2/9 Part 1 – Short answer – 46 points 1. Fill-in-the-blank or circle the best answer [18 points, -2 per wrong/blank, minimum 0] a. When using the algorithm we've named Tomasulo's 3, if you have N reorder- buffer entries, M reservation stations and K architected registers you can be sure that you will not have a use for more than N+K or N+M or M+K physical registers.

EECS 470 Midterm Exam Answers

the system (like the patch for computer vision models). Face classifiers do not work well for several groups of the population. Reward hacking: AI finding an unwanted / "hacky" solution to a

problem. c) (3 p oi n t) Y ou're choosing between two phones. One has an OLED screen and the other has an LCD display.